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Gerald T. Shekleton	(Depositor's name)
<i>Gerald T. Shekleton</i>	(Signature)
April 21, 2004	(Date)

APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
09/889,260	01/03/2002	Siegfried Hart	83256	1457

TITLE OF INVENTION: LOW JITTER ANALOG-DIGITAL LOCKED LOOP WITH LOCK DETECTION CIRCUIT

APPLN. TYPE	SMALL ENTITY	ISSUE FEE	PUBLICATION FEE	TOTAL FEE(S) DUE	DATE DUE
nonprovisional	NO	\$1330	\$0	\$1330	05/19/2004

EXAMINER	ART UNIT	CLASS-SUBCLASS
NGUYEN, MINH T	2816	327-156000

1. Change of correspondence address or indication of "Fee Address" (37 CFR 1.363).

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(A) NAME OF ASSIGNEE

(B) RESIDENCE: (CITY and STATE OR COUNTRY)

Infineon Technologies AG

Munchen, Germany

Please check the appropriate assignee category or categories (will not be printed on the patent); ☐ individual ☒ corporation or other private group entity ☐ government

4a. The following fee(s) are enclosed:

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(Authorized Signature) *Gerald T. Shekleton* (Date) *4/21/04*

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